



## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: JEFFREY W. SCOTT, ET AL.

Filed: February 22, 2002

For: CALIBRATED LOW-NOISE CURRENT AND VOLTAGE  
REFERENCES AND ASSOCIATED METHODS

Serial No.: 10/081,121 ✓

Group Art Unit: UNKNOWN

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Examiner: UNKNOWN

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Atty Docket No.: SILA:095

Technology Center 2600

Pursuant to 37 C.F.R. 1.8, I certify that this correspondence is being deposited with the U.S. Postal Service in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231 on the date below:

5-22-02

*Diane C Potts*

Date

Name

Assistant Commissioner For Patents  
Washington, D.C. 20231

**PRELIMINARY AMENDMENT**

Please amend the application as follows.

**In the specification:**

The rewritten clean versions of all the specification changes are provided below.

Attached at the end of this paper is an Appendix providing an indication of the changes relative to the prior version of the specification, as now required by Rule 121.

Please replace the paragraph beginning on page 1, line 27 and ending on page 2, line 17 with the following:

Furthermore, this patent application incorporates by reference the following patent documents: U.S. Patent Application Serial No. 10/075,122, Attorney Docket No. SILA:078, titled "Digital Architecture for Radio-Frequency Apparatus and Associated

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990.00

06/06/2002 MAHMEED1 000000032 10081121

01 FC:102  
02 FC:103